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Amendments to the Claims:

Please replace all prior versions, and listings of claims in the application with the following listing of claims.

Listing of claims

Claims 1-5 (canceled)

Claim 6 (new): Apparatus for a computer system comprising:

a bus architecture;

a plurality of modules connected to the bus architecture;

means for receiving data requests from modules for respective required data packets, each request including address data indicating the location of the data packet concerned, and sequence data indicating the timing of the request relative to other data requests issued by the module concerned;

means for receiving returned data packets and associated sequence data;

a storage area associated with each requesting module for storing the returned data packets in respective storage locations therein, in an order indicated by the associated sequence data; and

transfer means for transferring data packets from the storage area to the requesting module in the order of the storage locations in the storage area.

Claim 7 (new): Apparatus as claimed in claim 6, wherein the transfer means is operable to cycle through the storage locations of the storage area.

Claim 8 (new): Apparatus as claimed in claim 6, wherein the storage area associated with each requesting module is provided by a dual port RAM device.

Claim 9 (new): A computer system comprising at least one external storage device and apparatus as claimed in claim 6.

Claim 10 (new): A computer system comprising apparatus as claimed in claim 6.

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Claim 11 (new): An integrated circuit comprising apparatus as claimed in claim 6.

Claim 12 (new): A graphics processing system comprising apparatus as claimed in claim 6.

Claim 13 (new): A games console comprising apparatus as claimed in claim 6.